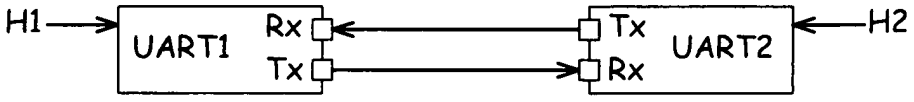

Fig. 1

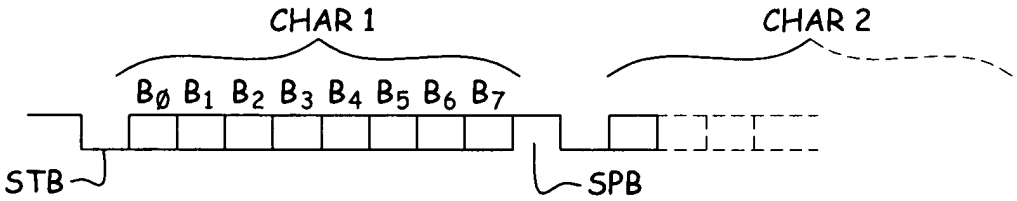

Fig. 2

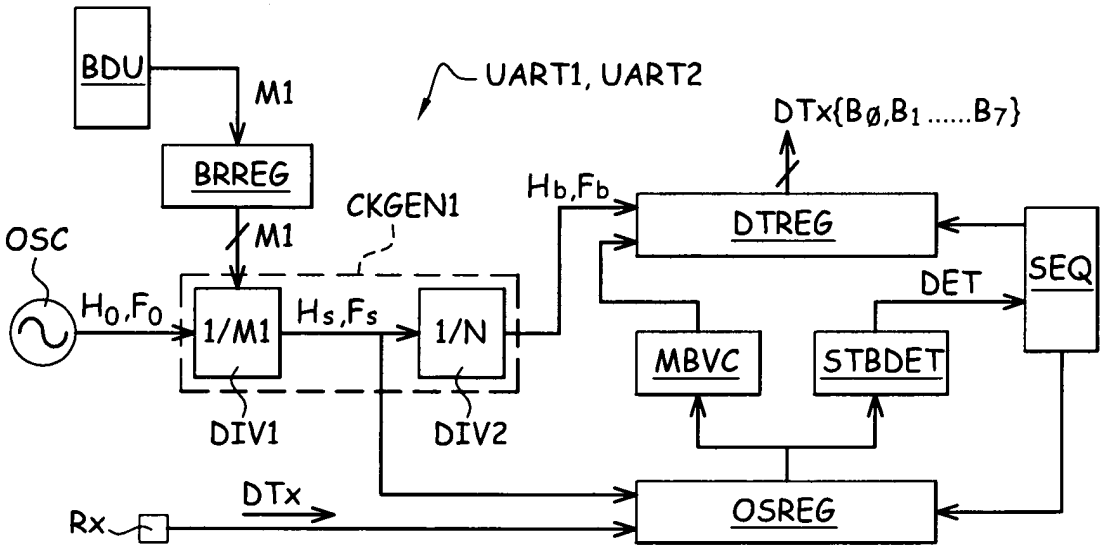

Fig. 3

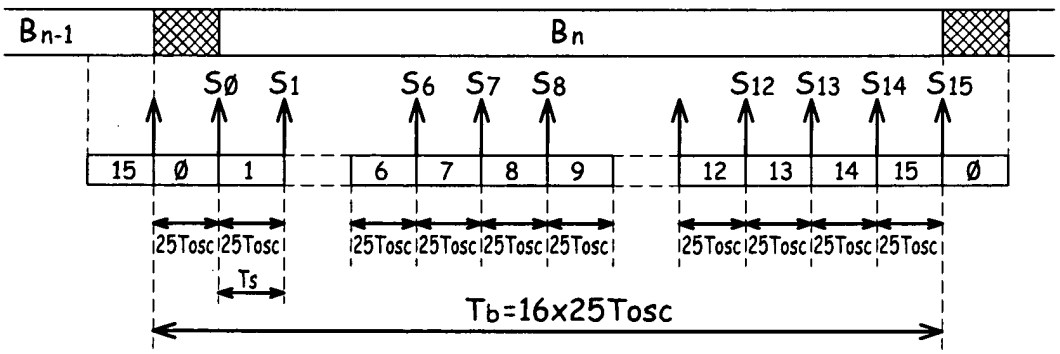
Bn-1
Bn
Sø S1 S6 S7 S8 S12 S13 S14 S15
15 ø 1 6 7 8 9 12 13 14 15 ø
25Tosc 25Tosc 25Tosc 25Tosc 25Tosc 25Tosc 25Tosc 25Tosc 25Tosc
Ts
Tb=16x25Tosc

Fig. 4

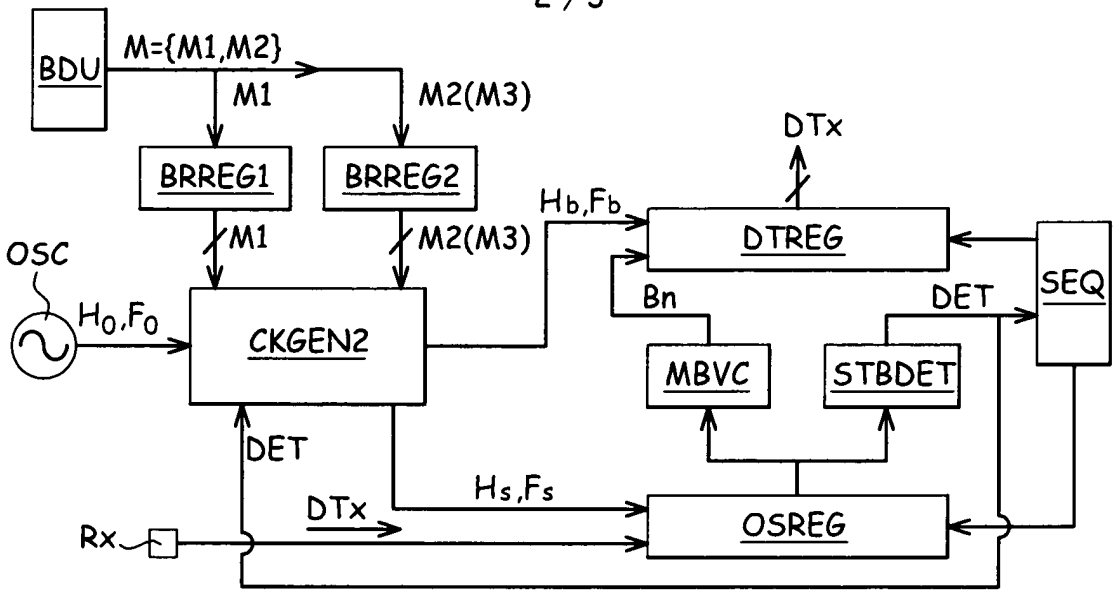
BDU
M={M1,M2}
M1
M2(M3)
BRREG1
BRREG2
DTx
Hb,Fb
DTREG
SEQ
OSC
M1
M2(M3)
Bn
DET
H0,F0
CKGEN2
MBVC
STBDET
DET
Hs,Fs
DTx
OSREG
Rx

Fig. 5

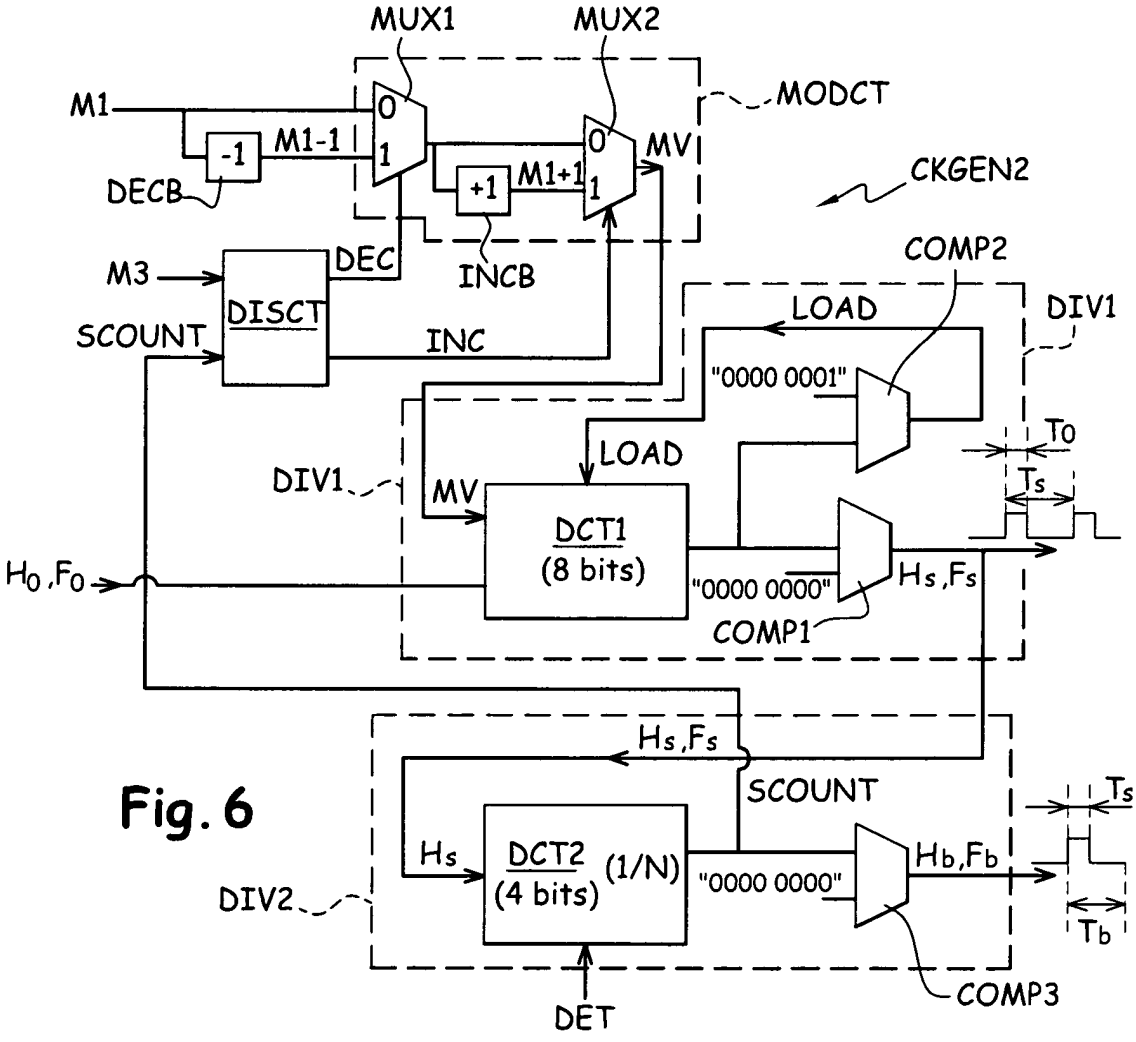
MUX1
MUX2
MODCT
M1
0
1
-1
M1-1
DECB
0
1
MV
+1
M1+1
CKGEN2
M3
DISCT
DEC
INCB
COMP2
SCOUNT
INC
LOAD
DIV1
"0000 0001"
T0
LOAD
DIV1
MV
Ts
DCT1
(8 bits)
H0,F0
"0000 0000"
Hs,Fs
COMP1
Hs,Fs
SCOUNT
Ts
Hs
DCT2
(4 bits)
(1/N)
"0000 0000"
Hb,Fb
DIV2
Tb
COMP3
DET

Fig. 6

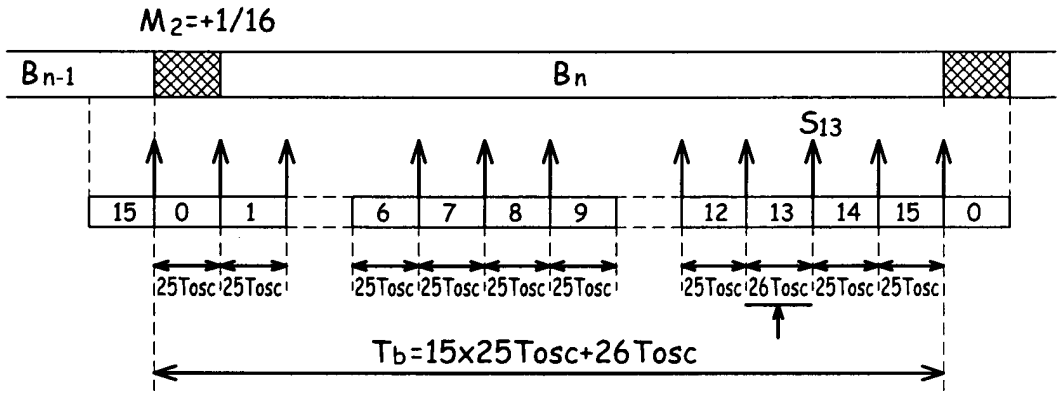

Fig. 7

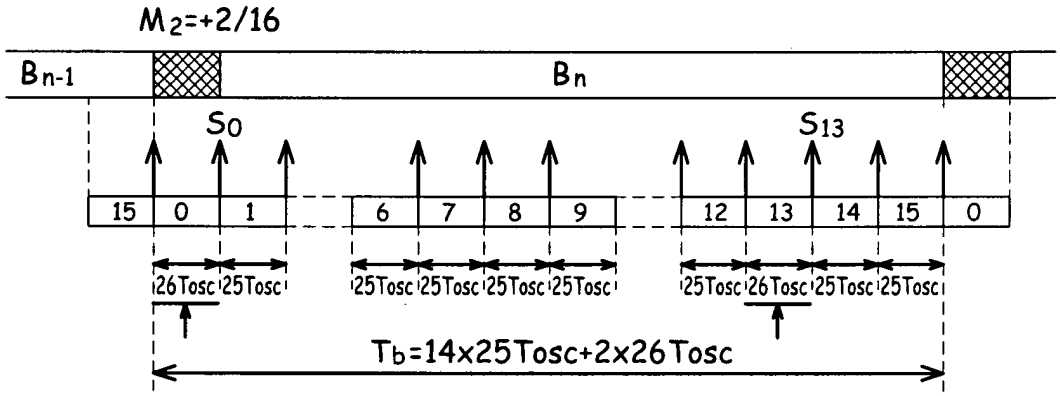

Fig. 8

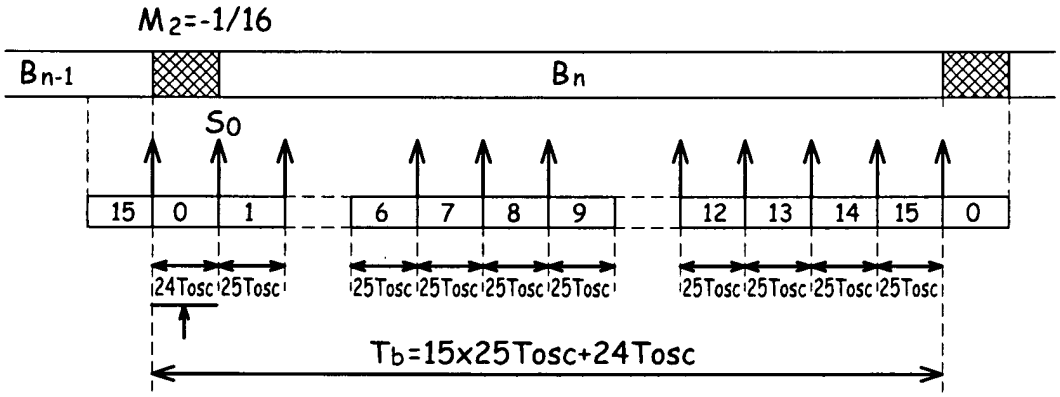

Fig. 9